

177

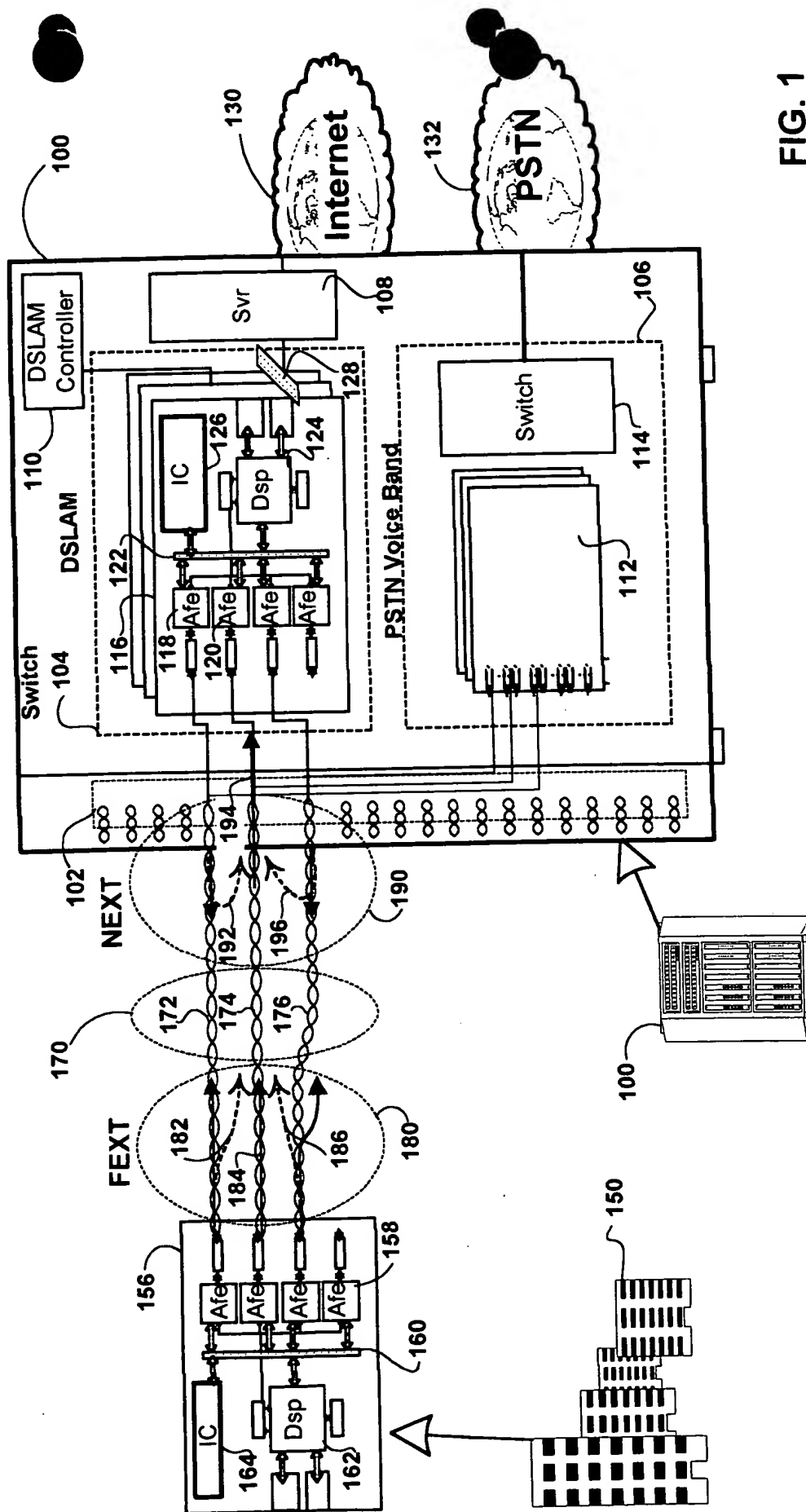


FIG. 1

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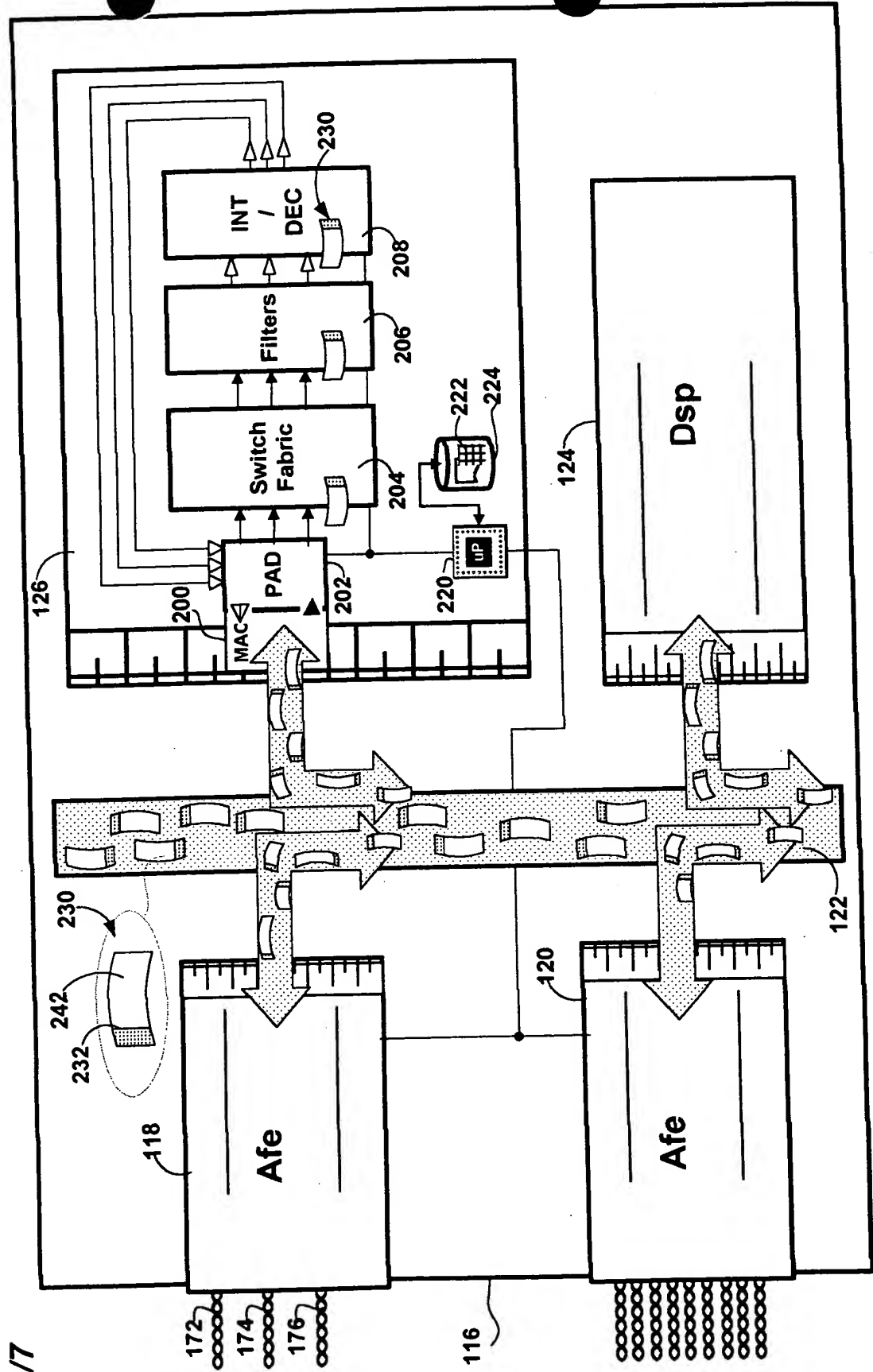


FIG. 2

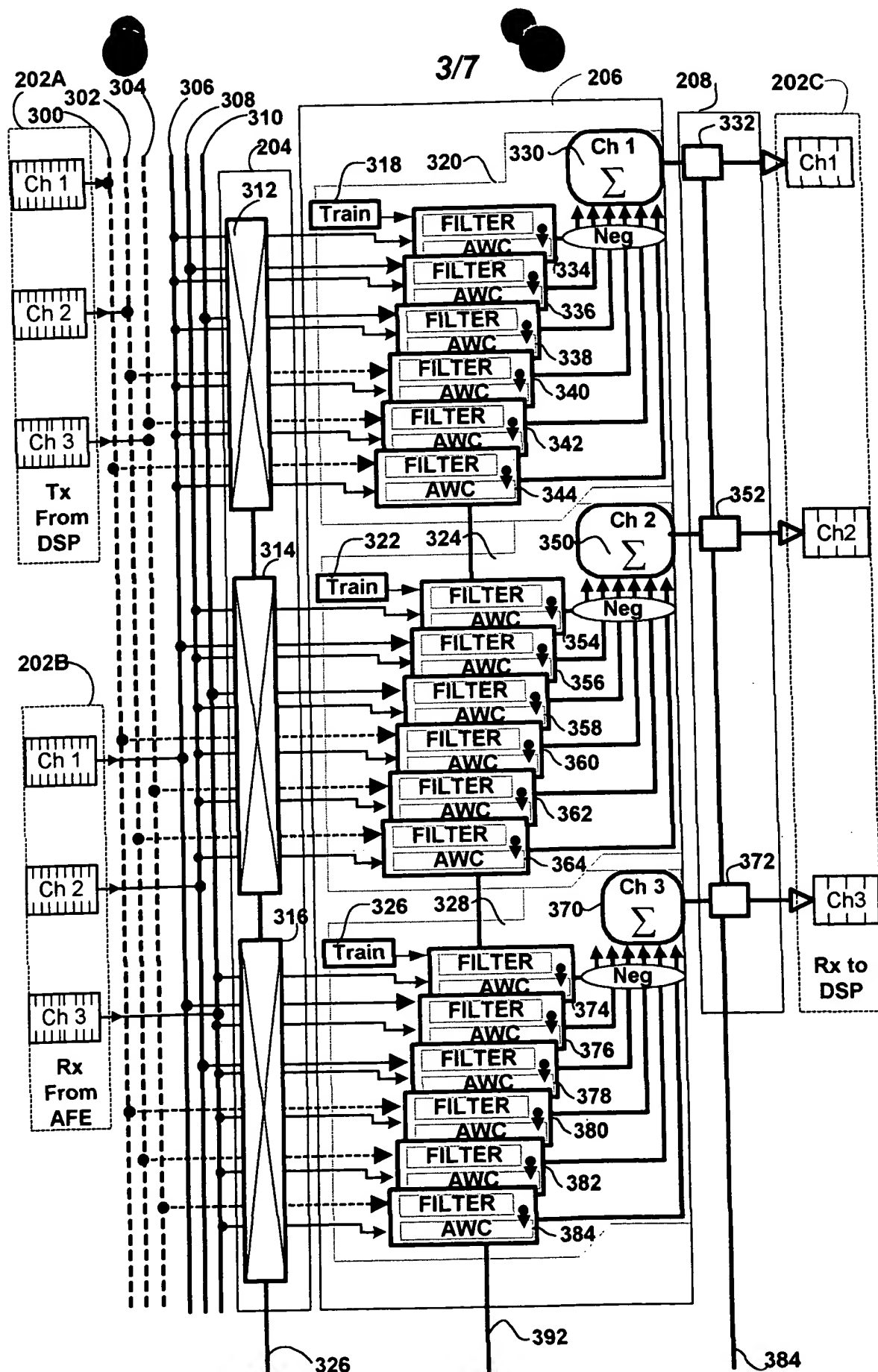


FIG. 3

FIG. 4 is a block diagram of a digital filter system. The system includes a coefficient generator (412) that provides coefficients (414, 416, 418) to a series of multipliers (432, 436, 440). The input signal (400) is processed through a series of adders (434, 438, 442) and subtractors (414, 416, 418) to produce the output signal (404). The system also includes a feedback loop (402) that feeds back the output signal (404) to the input of the first adder (434). The system is implemented in a digital filter (320) and is controlled by a control signal (330).

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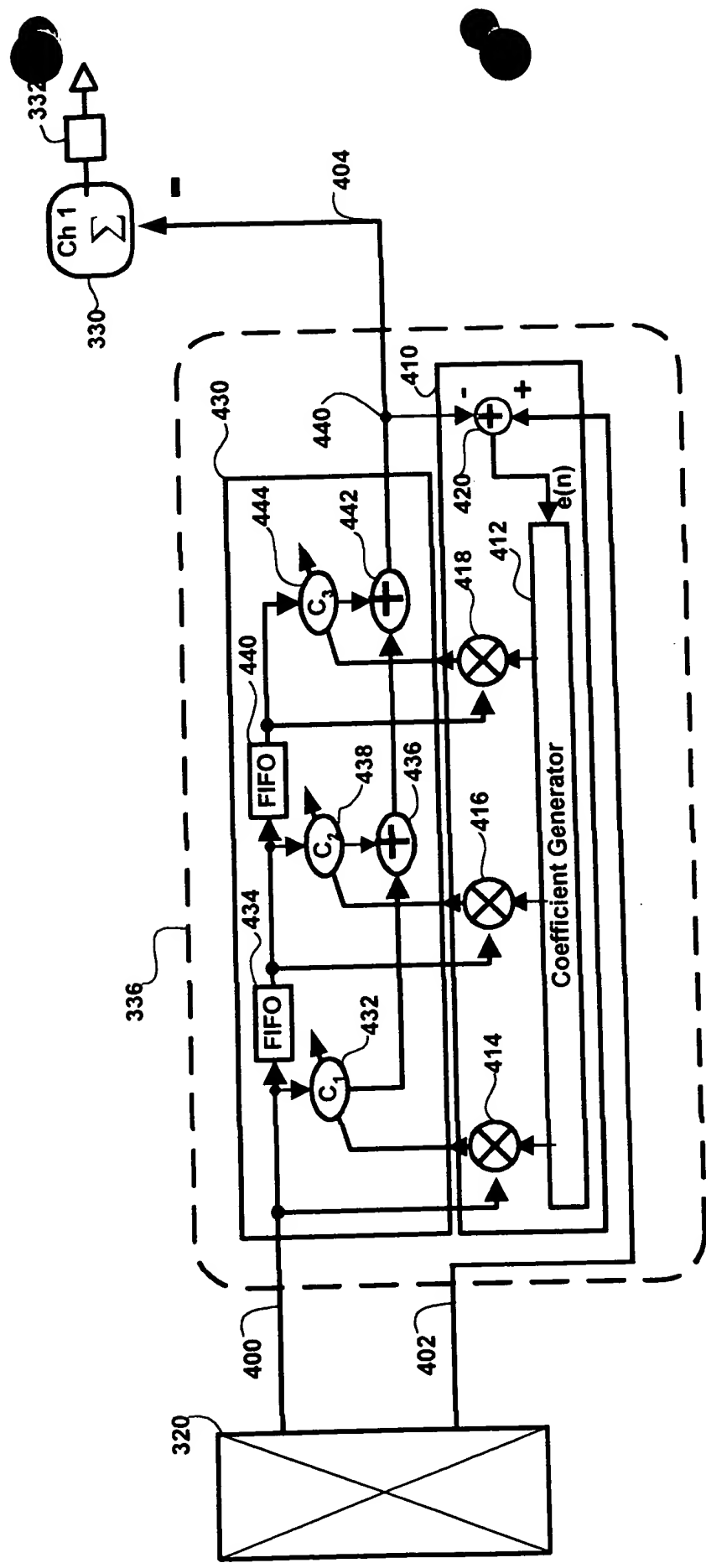


FIG. 4

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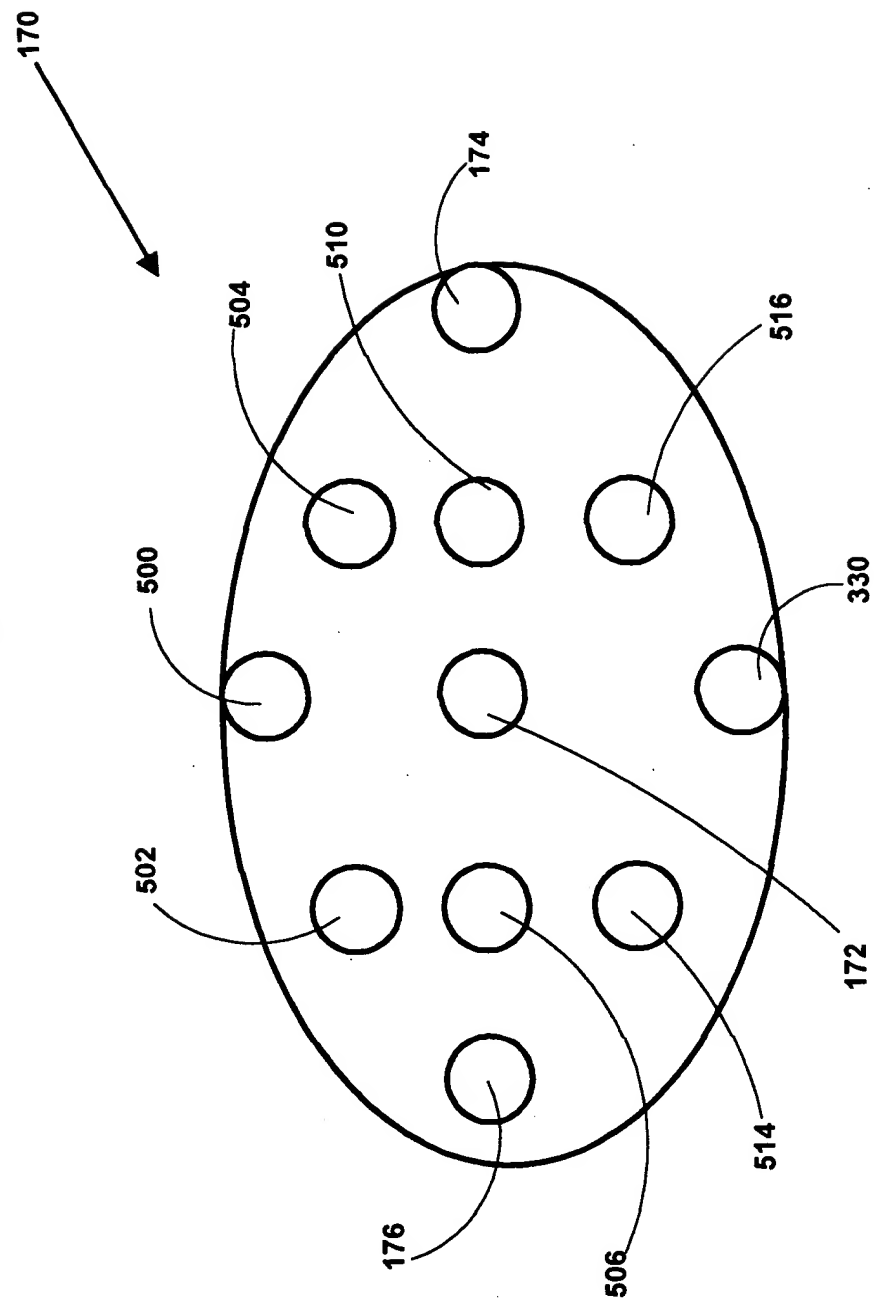


FIG. 5

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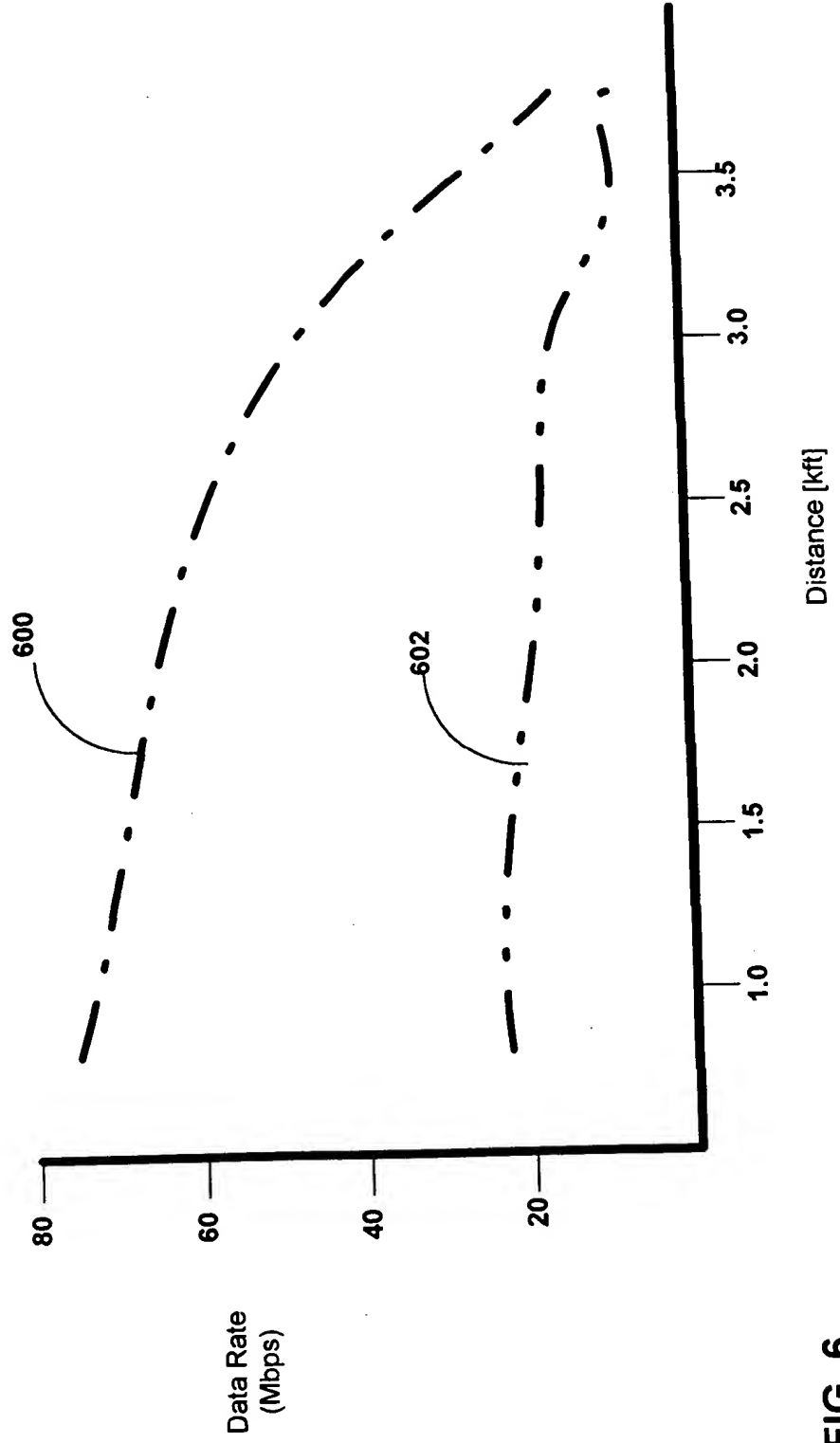


FIG. 6

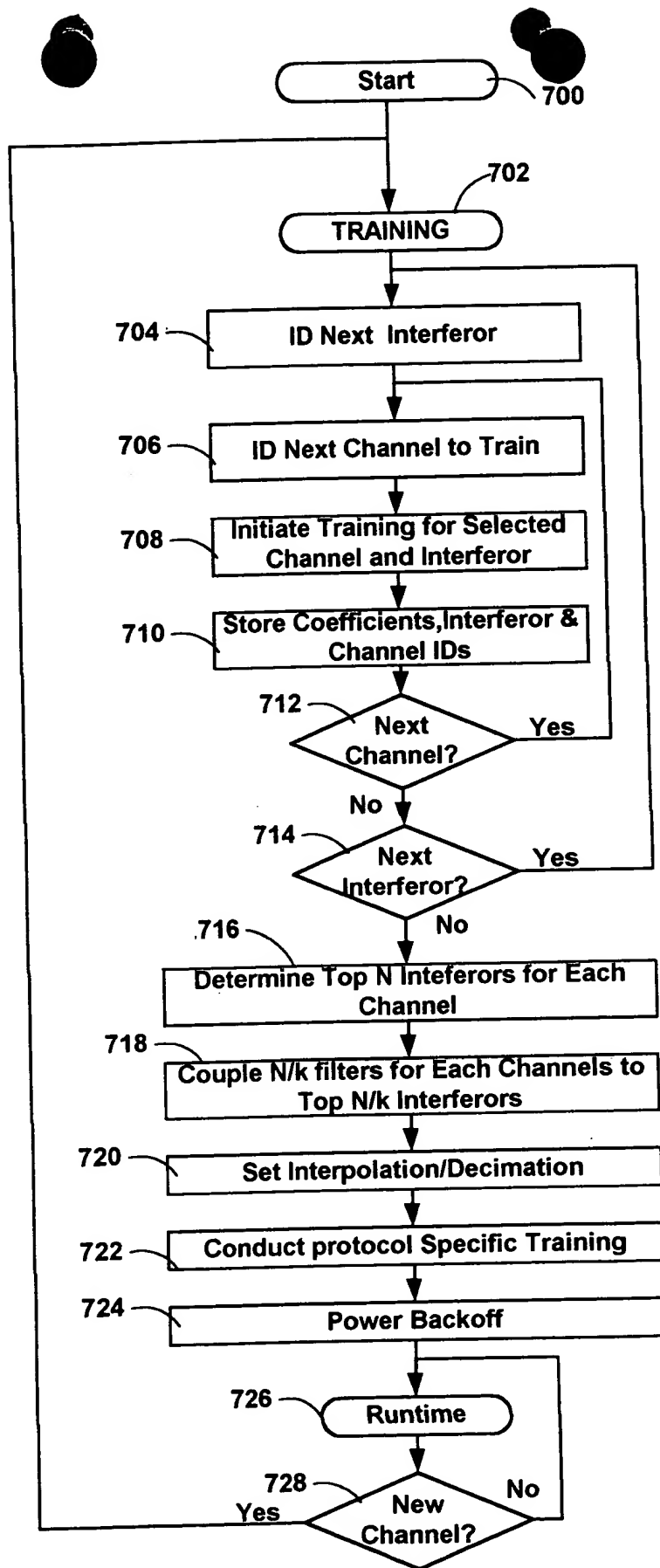


FIG. 7